# Integrated Silicon Pressure Sensor for Manifold Absolute Pressure, Altimeter or Barometer Applications On-Chip Signal Conditioned, Temperature Compensated and Calibrated

Motorola's MPX4115A/MPXA4115A series sensor integrates on—chip, bipolar op amp circuitry and thin film resistor networks to provide a high output signal and temperature compensation. The small form factor and high reliability of on—chip integration make the Motorola pressure sensor a logical and economical choice for the system designer.

The MPX4115A/MPXA4115A series piezoresistive transducer is a state-of-the-art, monolithic, signal conditioned, silicon pressure sensor. This sensor combines advanced micromachining techniques, thin film metallization, and bipolar semiconductor processing to provide an accurate, high level analog output signal that is proportional to applied pressure.

Figure 1 shows a block diagram of the internal circuitry integrated on a pressure sensor chip.

#### **Features**

- 1.5% Maximum Error over 0° to 85°C
- Ideally suited for Microprocessor or Microcontroller— Based Systems
- Temperature Compensated from −40° to +125°C
- Durable Epoxy Unibody Element or Thermoplastic (PPS) Surface Mount Package

#### **Application Examples**

- · Aviation Altimeters
- Industrial Controls
- Engine Control
- Weather Stations and Weather Reporting Devices

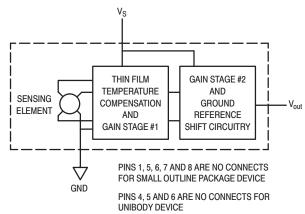
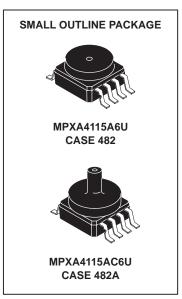


Figure 1. Fully Integrated Pressure Sensor Schematic

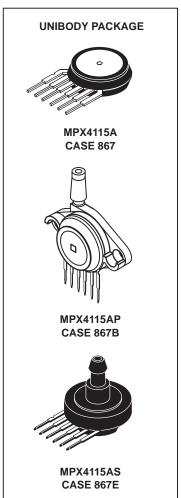


	PIN NUMBER						
	1	N/C	5	N/C			
ſ	2 V <sub>S</sub>		6	N/C			
ſ	3	Gnd	7	N/C			
ſ	4	V <sub>out</sub>	8	N/C			

NOTE: Pins 1, 5, 6, 7, and 8 are internal device connections. Do not connect to external circuitry or ground. Pin 1 is noted by the notch in the lead.

## MPX4115A MPXA4115A SERIES

INTEGRATED
PRESSURE SENSOR
15 to 115 kPa (2.2 to 16.7 psi)
0.2 to 4.8 Volts Output



PIN NUMBER						
1	V <sub>out</sub>	4	N/C			
2	Gnd	5	N/C			
3	Vs	6	N/C			

NOTE: Pins 4, 5, and 6 are internal device connections. Do not connect to external circuitry or ground. Pin 1 is noted by the notch in the lead.



REV 4

# MPX4115A MPXA4115A FERESCALE SEMICONDUCTOR, INC. 2005 MAXIMUM RATINGS(NOTE)

Parametrics			Units
Maximum Pressure (P1 > P2)	P <sub>max</sub>	400	kPa
Storage Temperature	T <sub>stg</sub>	–40° to +125°	°C
Operating Temperature	T <sub>A</sub>	–40° to +125°	°C

NOTE: Exposure beyond the specified limits may cause permanent damage or degradation to the device.

#### **OPERATING CHARACTERISTICS** ( $V_S = 5.1 \text{ Vdc}$ , $T_A = 25^{\circ}\text{C}$ unless otherwise noted, P1 > P2. Decoupling circuit shown in Figure 3 required to meet Electrical Specifications.)

Chara	cteristic	Symbol	Min	Тур	Max	Unit
Pressure Range		P <sub>OP</sub>	15	_	115	kPa
Supply Voltage <sup>(1)</sup>		V <sub>S</sub>	4.85	5.1	5.35	Vdc
Supply Current		Ιο	_	7.0	10	mAdc
Minimum Pressure Offset <sup>(2)</sup> @ V <sub>S</sub> = 5.1 Volts	(0 to 85°C)	V <sub>off</sub>	0.135	0.204	0.273	Vdc
Full Scale Output <sup>(3)</sup> @ V <sub>S</sub> = 5.1 Volts	(0 to 85°C)	V <sub>FSO</sub>	4.725	4.794	4.863	Vdc
Full Scale Span <sup>(4)</sup> @ V <sub>S</sub> = 5.1 Volts	(0 to 85°C)	V <sub>FSS</sub>	4.521	4.590	4.659	Vdc
Accuracy <sup>(5)</sup>	(0 to 85°C)	_	_	_	±1.5	%V <sub>FSS</sub>
Sensitivity		V/P	_	45.9	_	mV/kPa
Response Time <sup>(6)</sup>		t <sub>R</sub>	_	1.0	_	ms
Output Source Current at Full Scale Output		I <sub>O+</sub>	_	0.1	_	mAdc
Warm-Up Time <sup>(7)</sup>		_	_	20	_	ms
Offset Stability <sup>(8)</sup>		_	_	±0.5	_	%V <sub>FSS</sub>

#### NOTES:

- 1. Device is ratiometric within this specified excitation range.
- 2. Offset (Voff) is defined as the output voltage at the minimum rated pressure.
- 3. Full Scale Output (V<sub>FSO</sub>) is defined as the output voltage at the maximum or full rated pressure.
- 4. Full Scale Span (V<sub>FSS</sub>) is defined as the algebraic difference between the output voltage at full rated pressure and the output voltage at the minimum rated pressure.
- 5. Accuracy is the deviation in actual output from nominal output over the entire pressure range and temperature range as a percent of span at 25°C due to all sources of error including the following:
  - · Linearity: Output deviation from a straight line relationship with pressure over the specified pressure range.
  - Temperature Hysteresis: Output deviation at any temperature within the operating temperature range, after the temperature is

cycled to and from the minimum or maximum operating temperature points, with zero differential

pressure applied.

Output deviation at any pressure within the specified range, when this pressure is cycled to and from Pressure Hysteresis:

minimum or maximum rated pressure at 25°C.

TcSpan: Output deviation over the temperature range of 0° to 85°C, relative to 25°C.

TcOffset: Output deviation with minimum pressure applied, over the temperature range of 0° to 85°C, relative

to 25°C.

- 6. Response Time is defined as the time for the incremental change in the output to go from 10% to 90% of its final value when subjected to a specified step change in pressure.
- 7. Warm-up Time is defined as the time required for the product to meet the specified output voltage after the pressure has been stabilized.
- 8. Offset Stability is the product's output deviation when subjected to 1000 cycles of Pulsed Pressure, Temperature Cycling with Bias Test.

#### **MECHANICAL CHARACTERISTICS**

Characteristics	Тур	Unit
Weight, Basic Element (Case 867)	4.0	grams
Weight, Small Outline Package (Case 482)	1.5	grams

## Freescale Semiconductor PRG 15A MPXA4115A SERIES

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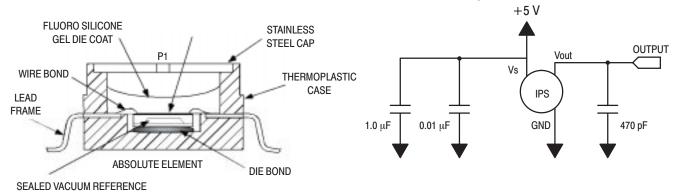


Figure 2. Cross Sectional Diagram SOP (not to scale)

Figure 3. Recommended power supply decoupling and output filtering.

For additional output filtering, please refer to Application Note AN1646.

Figure 2 illustrates the absolute sensing chip in the basic chip carrier (Case 482).

Figure 3 shows the recommended decoupling circuit for interfacing the output of the integrated sensor to the A/D input of a microprocessor or microcontroller. Proper decoupling of the power supply is recommended.

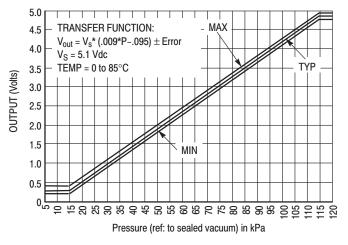


Figure 4. Output versus Absolute Pressure

Figure 4 shows the sensor output signal relative to pressure input. Typical minimum and maximum output curves are shown for operation over 0 to 85°C temperature range. The output will saturate outside of the rated pressure range.

A fluorosilicone gel isolates the die surface and wire bonds from the environment, while allowing the pressure signal to be transmitted to the silicon diaphragm. The MPX4115A/MPXA4115A series pressure sensor operating characteristics, internal reliability and qualification tests are based on use of dry air as the pressure media. Media other than dry air may have adverse effects on sensor performance and long–term reliability. Contact the factory for information regarding media compatibility in your application.

### MPX4115A MPXA4115A Freescale Semiconductor, Inc.

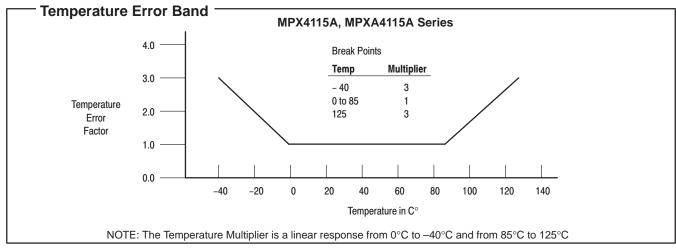
Transfer Function (MPX4115A, MPXA4115A)

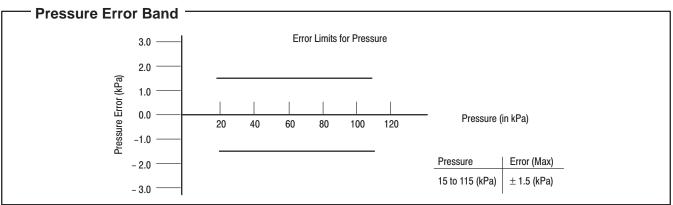
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Nominal Transfer Value:  $V_{out} = V_S x (0.009 x P - 0.095)$ 

± (Pressure Error x Temp. Factor x 0.009 x V<sub>S</sub>)

 $V_S = 5.1 \pm 0.25 \text{ Vdc}$ 





#### **ORDERING INFORMATION — UNIBODY PACKAGE**

Device Type	Options	Case No.	MPX Series Order No.	Marking
Basic Element	Absolute, Element Only	867	MPX4115A	MPX4115A
Ported Elements	Absolute, Ported	867B	MPX4115AP	MPX4115AP
	Absolute, Stove Pipe Port	867E	MPX4115AS	MPX4115A

#### **ORDERING INFORMATION — SMALL OUTLINE PACKAGE**

Device Type	Options	Case No.	MPX Series Order No.	Packing Options	Marking
Basic Element	Absolute, Element Only	482	MPXA4115A6U	Rails	MPXA4115A
	Absolute, Element Only	482	MPXA4115A6T1	Tape and Reel	MPXA4115A
Ported Element	Absolute, Axial Port	482A	MPXA4115AC6U	Rails	MPXA4115A
	Absolute, Axial Port	482A	MPXA4115AC6T1	Tape and Reel	MPXA4115A

## Freescale Semiconductor, Jac MPXA4115A SERIES

# ARCHIVED BY FREESCALE SEMICONDUCTOR, INC. 2005 INFORMATION FOR USING THE SMALL OUTLINE PACKAGE (CASE 482)

#### MINIMUM RECOMMENDED FOOTPRINT FOR SURFACE MOUNTED APPLICATIONS

Surface mount board layout is a critical portion of the total design. The footprint for the surface mount packages must be the correct size to ensure proper solder connection interface between the board and the package. With the correct fottprint, the packages will self-align when subjected to a solder reflow process. It is always recommended to design boards with a solder mask layer to avoid bridging and shorting between solder pads.

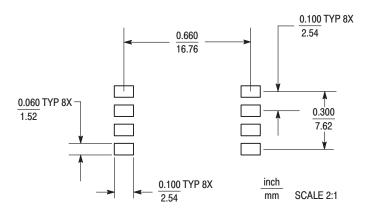
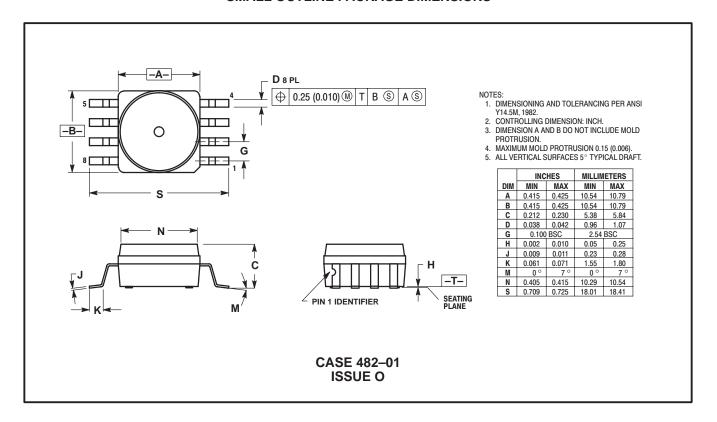
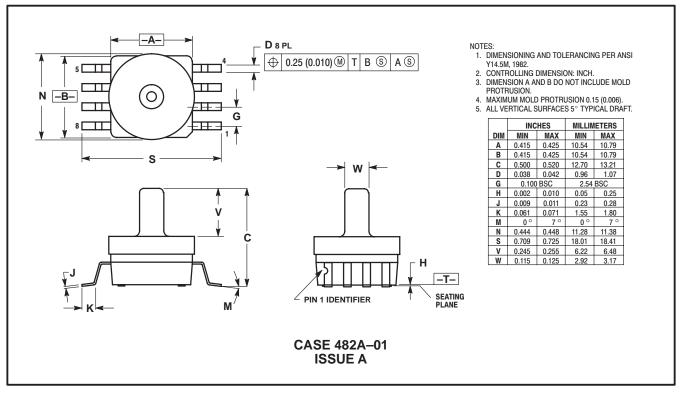


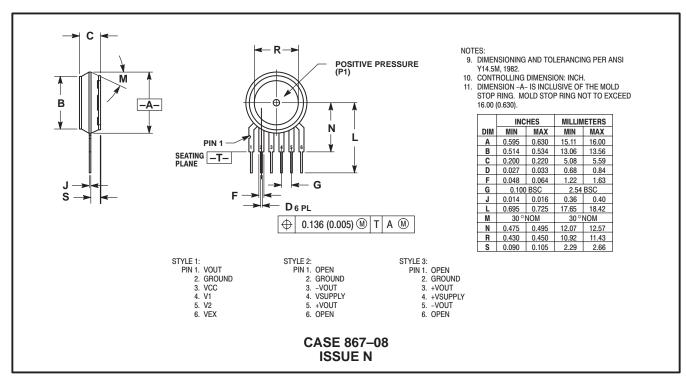
Figure 5. SOP Footprint (Case 482)

# MPX4115A MPXA4115A SERESCALE SEMICONDUCTOR, INC. 2005 ARCHIVED BY FREESCALE SEMICONDUCTOR, INC. 2005 SMALL OUTLINE PACKAGE DIMENSIONS

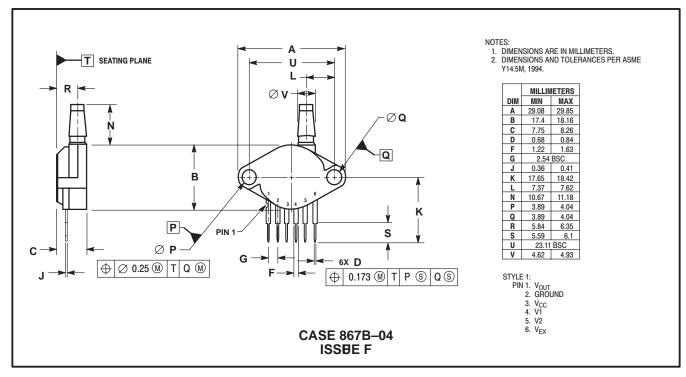




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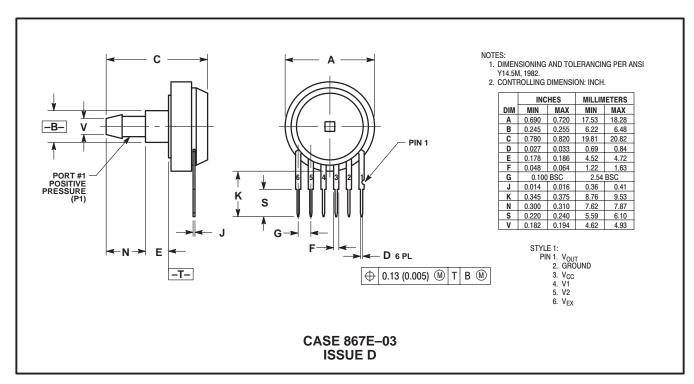


#### **BASIC ELEMENT**



PRESSURE SIDE PORTED (AP, GP)

# MPX4115A MPXA4115A SERESCALE SEMICONDUCTOR, INC. 2005 UNIBODY PACKAGE DIMENSIONS—CONTINUED



PRESSURE SIDE PORTED (AS, GS)

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